

(19) World Intellectual Property  
Organization  
International Bureau



(43) International Publication Date  
31 December 2003 (31.12.2003)

PCT

(10) International Publication Number  
**WO 2004/001857 A1**

(51) International Patent Classification<sup>7</sup>: **H01L 31/028**,  
31/18

(74) Agents: **CONNORS, Matthew, E. et al.**; Samuels,  
Gauthier & Stevens, LLP, 225 Franklin Street, Suite 3300,  
Boston, MA 02110 (US).

(21) International Application Number:  
PCT/US2002/038283

(81) Designated States (*national*): AE, AG, AL, AM, AT, AU,  
AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU,  
CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH,  
GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC,  
LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW,  
MX, MZ, NO, NZ, OM, PH, PL, PT, RO, RU, SD, SE, SG,  
SK, SL, TJ, TM, TN, TR, TT, TZ, UA, UG, UZ, VN, YU,  
ZA, ZM, ZW.

(22) International Filing Date: 2 December 2002 (02.12.2002)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:  
60/389,819 19 June 2002 (19.06.2002) US

(84) Designated States (*regional*): ARIPO patent (GH, GM,  
KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW),  
Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM),  
European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE,  
ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE, SI, SK,  
TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ,  
GW, ML, MR, NE, SN, TD, TG).

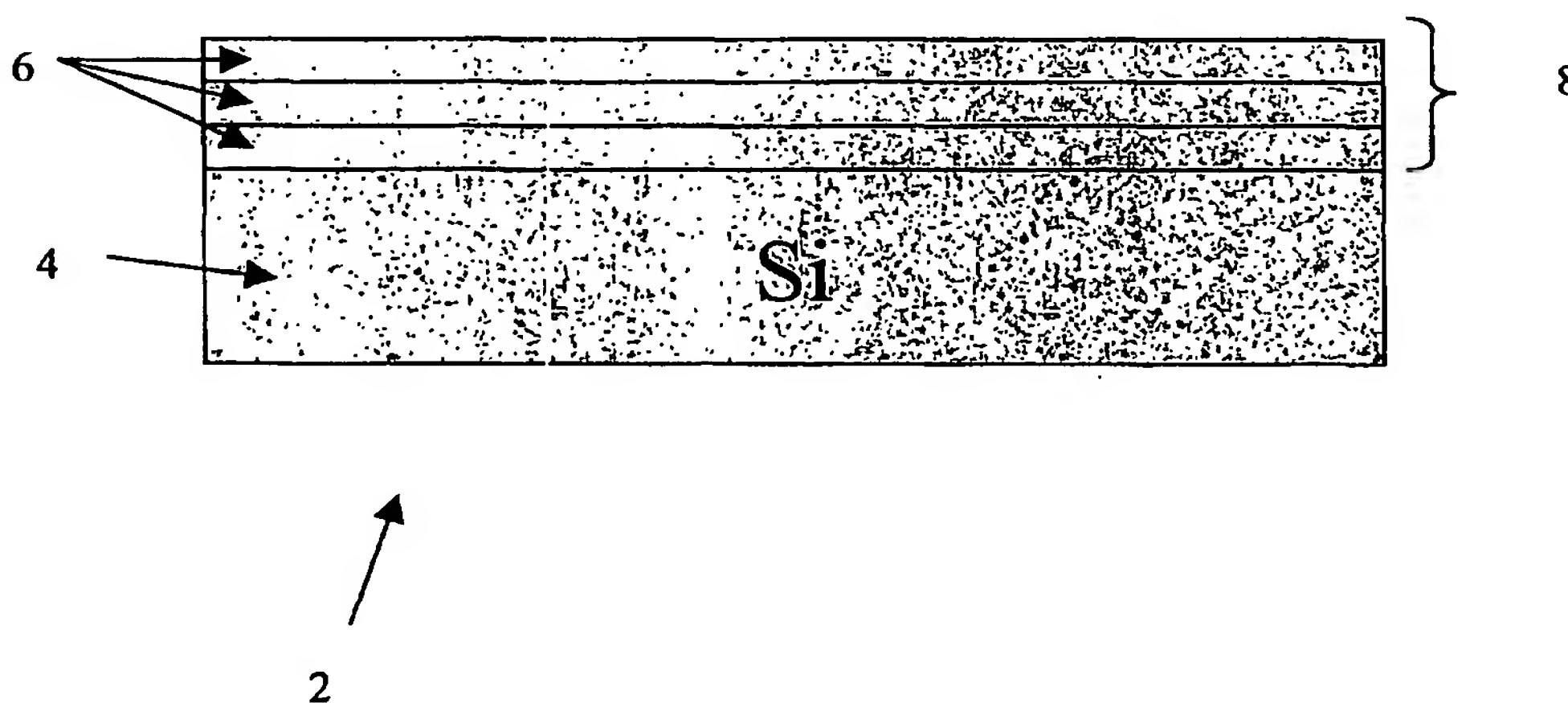
(71) Applicant: **MASSACHUSETTS INSTITUTE OF  
TECHNOLOGY** [US/US]; 77 Massachusetts Avenue,  
Cambridge, MA 02139 (US).

(72) Inventors: **WADA, Kazumi**; 23 Hill Street, Lexington,  
MA 02421 (US). **CANNON, Douglas, D.**; 264 Wilbur  
Ave. #1, Somerville, MA 02144 (US). **LIU, Jifeng**; 187  
Windsor Street, Apt. # 2, Cambridge, MA 02139 (US).  
**KIMERLING, Lionel, C.**; 369 Strawberry Hill Road,  
Concord, MA 01742 (US). **ISHIKAWA, Yasuhiko**; 37  
Sherman Street # 3, Cambridge, MA 02138 (US).

Published:  
— with international search report

For two-letter codes and other abbreviations, refer to the "Guid-  
ance Notes on Codes and Abbreviations" appearing at the begin-  
ning of each regular issue of the PCT Gazette.

(54) Title: **GE PHOTODETECTORS**



(57) Abstract: A photodetector device includes a plurality of Ge epilayers that are grown on a substrate and annealed in a defined temperature range. The Ge epilayers form a tensile strained Ge layer that allows the photodetector device to operate efficiently in the C-band and L-band.

WO 2004/001857 A1

**GE PHOTODETECTORS****PRIORITY INFORMATION**

This application claims priority from provisional application Ser. No.  
5 60/389,819 filed June 19, 2002, which is incorporated herein by reference.

**BACKGROUND OF THE INVENTION**

The invention relates to the field of photodetectors, and in particular to monolithically integrated Ge photodetectors on Si.

10 Photodetectors are fundamental devices that convert optical signals into electric signals. Fiber optical communication employs 1300 and 1550 nm wavelengths because of low attenuation coefficients of silica fibers. Er doped fiber amplifiers emphasize the importance of 1550 nm because of the direct amplification of optical signals without converting to electric signals. The amplification range  
15 between 1530-1560 nm is referred to as C-band, and the recently extended amplification range between 1570-1608nm is referred to as L-band. The photodetectors for 1550 nm detection have so far been InGaAs photodetectors, since InGaAs is a direct semiconductor whose bandgap is 0.75 eV (corresponding to 1653nm). Thus, InGaAs photodetectors can convert any optical signal in the C- and  
20 L- bands to electric signals. These optical fiber communication components are well developed.

Recently, optical technology has expanded its territory from fiber communication to photonic integrated circuits on a chip. This allows for high speed and broad band communication. The impact is even larger if optics is merged into  
25 Si LSIs, e.g., 10 GHz clock processors, etc. InGaAs photodetectors are not easy to implement on a silicon chip, since InGaAs is a III-V compound semiconductor. In general, the elements In, Ga, and As are all dopants in silicon to show donor or acceptor characteristics and could thus alter the circuit performance if diffused. Ge can be a candidate for on-chip photodetectors, since Ge is in the group IV element  
30 and produces no harmful effects if diffused. Thus, Ge provides a perfect opportunity to form highly efficient photodetectors.

**SUMMARY OF THE INVENTION**

According to one aspect of the invention, there is provided a photodetector

device. The photodetector device includes a plurality of Ge epilayers that are grown on a silicon substrate and annealed in a defined temperature range. The Ge epilayers form a tensile strained Ge layer that allows the photodetector device to operate efficiently in the C-band and L-band.

5        According to another aspect of the invention, there is provided a method of forming a photodetector device. The method includes forming a plurality of Ge epilayers that are grown on a substrate. Moreover, the method includes annealing the Ge epilayers in a defined temperature range. Furthermore, the method includes developing a tensile strained Ge layer using the annealed Ge epilayers, the tensile  
10        strained Ge layer allowing the photodetector device to operate efficiently in the C-band and L-band.

      According to another aspect of the invention, there is provided a photodetector device. The photodetector device includes a plurality of SiGe epilayers that are grown on a substrate at a high temperature so as to form a SiGe  
15        structure. The SiGe layer forms a tensile strained SiGe layer by cooling to room temperature the SiGe structure using the bi-metal effect. The tensile strained SiGe layer allows the photodetector device to operate efficiently in the C-band and L-band.

      According to another aspect of the invention, there is provided a method of  
20        forming a photodetector device. The method includes growing a plurality of SiGe epilayers on a silicon substrate at a high temperature so as to form a SiGe structure. Furthermore, the method includes forming a tensile strained SiGe layer by cooling to room temperature the SiGe structure using the bi-metal effect. The tensile strained SiGe layer allows the photodetector device to operate efficiently in the C-  
25        band and L-band.

#### **BRIEF DESCRIPTION OF THE DRAWINGS**

FIG. 1 is schematic block diagram illustrating the techniques used to create a Ge photodetector using a tensile strained Ge layer;

30        FIG. 2 is an energy band diagram of Ge under stress;

FIG. 3 is a graph of the absorption of a Ge epilayer grown using the invention;

FIG. 4 is a graph of an optical bandgap of a Ge epilayer and a bulk Ge

analyzed using photoreflectance spectroscopy;

FIG. 5 is a graph demonstrating the tensile strain in Ge shrinking its direct bandgap ( $E_g$ );

FIG. 6 is a X-ray locking curve measurement of the annealed tensile Ge epilayer.

#### **DETAILED DESCRIPTION OF THE INVENTION**

Ge is an indirect semiconductor whose bandgap is 0.667 eV corresponding to 1850nm. Because of indirect bandgap characteristics, the absorption coefficient is small, approximately  $100\text{cm}^{-1}$  depending on the wavelength. The direct bandgap of Ge is 0.8eV corresponding to 1550nm. Thus, the photodetector performance of Ge is marginally good in the C-band but, is poor in the L-band. To overcome the drawback of Ge, various attempts have been used, such as GeSn alloy, to decrease the bandgap.

One goal in Ge photodetectors is to shrink the bandgap to cover the C- and L- bands with high efficiency. The invention uses tensile strain to accomplish this task.

FIG. 1 is schematic block diagram illustrating the techniques used to create a Ge photodetector 2 using a tensile strained Ge layer 8. The Ge photodetector is formed on a Si wafer. This is ideal for realizing Si-based optoelectronic circuits as well as optical interconnection in Si-LSIs. In spite of a large lattice mismatch (4 %) between Ge and Si, a layer 8 of Ge is grown directly on the Si wafer 4 with uniform thickness and with low density of threading dislocations, using an ultrahigh-vacuum chemical vapor deposition (UHV-CVD) technique. Other depositing techniques can be used in accordance with this invention.

By growing the Ge layer 8 directly on the Si, the bandgap shrinkage is shown to be induced by tensile strain. The Ge layer 8 includes a defined number of Ge epilayers 6, grown at 700°C on the heavily-doped *p*-Si (100) wafer using the UHV-CVD technique, which also uses  $\text{GeH}_4$  (15% in Ar) as the deposition gas. After the growth, cyclic thermal annealing (10 cycles between 900°C and 700°C) is performed to reduce the dislocation density. Absorption coefficients derived from responsibility spectra indicate substantial red shift compared with bulk Ge. It is shown from PR spectra that the red shift is induced by bandgap shrinkage. The Ge epilayers show approximately 0.77 eV, which is smaller by approximately 0.03 eV

than direct bandgap for bulk Ge. This property is beneficial for Ge photodetectors that can operate in the C-band as well as the L-band. The origin of the shrinkage is found to be not by electric-field-enhanced tunneling such as Franz-Keldysh effect. The pseudo potential calculation suggests that the bandgap shrinkage would occur  
5 under tensile strain of approximately 0.2%. It is discussed that generation of such tensile strain is ascribed to the difference of thermal expansion between Ge and Si.

Furthermore, this method is applicable for not only Ge epilayers 6, but also SiGe where Ge concentration is more than 50 % to introduce thermal mismatch. In the case of SiGe, the equilibrate temperature should be designed higher than 900 °C  
10 and preferably close to the melting temperature of the alloy. In this embodiment, Si is used as a substrate, however, other types of substrates can be used whose lattice expansion coefficient is smaller than Ge.

Another technique in creating tensile stress in Ge is to use the thermal mismatch between Ge and Si. The linear expansion coefficients of Ge and Si are  
15  $5.8 \times 10^{-6} \text{ K}^{-1}$  and  $2.6 \times 10^{-6} \text{ K}^{-1}$ , respectively. When the Ge epilayers 6 are grown on Si and then heat up to a high temperature after growth to equilibrate these lattices so as to have its own lattice constants, the tensile strain should be introduced in the Ge epilayers 6 by cooling to room temperature the SiGe structure in terms of the so called bi-metal effect.

20 According to this technique, isothermal annealing at 900 °C for 10 min or longer for Ge epilayers grown at 700 °C is performed to fully relax the Ge lattice constant; Ge has its own lattice constant. Also isothermal annealing at 900 °C for 30 min or longer for Ge epilayers grown at 600 °C is needed to fully relax the Ge lattice constant. Fig. 5 shows the theoretical limit of the bandgap shrinkage using  
25 the bi-metal effect. It is assumed that rapid thermal annealing is used to cool the Ge epilayers instantaneously to room temperature. This technique of rapid cooling of the Ge epilayer on Si should be more effective than slow cooling, since lattice relaxation could happen in slower cooling. It shows the maximum strain 0.32 % and the minimum bandgap 0.757 eV as shown in Fig. 2. It is marked that the bandgap  
30 corresponds to 1638 nm is much longer than the long wavelength edge of the L-band.

SiGe epilayers can also be grown using this technique. The isothermal annealing temperature can be approximately 1330 °C for  $\text{Si}_{0.5}\text{Ge}_{0.5}$  epilayers.



FIG. 2 is an energy band diagram of Ge under stress. Pseudo potential calculations have been used. The band diagram shows that the Ge bandgap shrinks when tensile stress is induced. The Ge direct bandgap consists of G valley and degenerated hole bands. When the tensile stress is introduced, the energy position of the G valley is lowered and that of the light hole band is higher, eventually the direct bandgap shrinks. FIG. 2 also shows that the tensile stress of 0.2 % is needed to shift the band edge up to 1600nm to cover the L-band. As noted above our maximum strain is 0.32 % and the band gap is 1638 nm which should cover the L band.

FIG. 3 is a graph of the absorption of a Ge epilayer grown using the invention. In particular, the graph shows the absorption coefficients of the Ge epilayer grown using the invention and bulk Ge. Furthermore, the graph shows increase sensitivity up to 1600nm between the C-band and L-band for the Ge epilayer. The Ge bulk does not demonstrate increase sensitivity in the C-band and practically no sensitivity in the L-band. Thus, using Ge epilayers as a tensile Ge layer can be beneficial for operations in the C-band and L-band with sufficient sensitivity. Using rapid thermal annealing the Ge epilayers can be cooled rapidly, leading further shrinkage of the bandgap.

FIG. 4 is a graph of an optical bandgap of a Ge epilayer and a bulk Ge analyzed using photoreflectance spectroscopy. The vertical axis is associated with bandgap energy and the horizontal axis is associated with the a constant value  $C_j = [3\pi(j-1/2)/2]^{2/3}$ , where j is an integer value that denotes the number of peaks and valleys in the spectrum counted from the longer wavelength. Overall, the graph demonstrates that a Ge epilayer grown at 700 °C and then heated up to 900 °C, using the annealed steps described previously, and cooled rapidly shows a narrower bandgap than bulk Ge. Furthermore, the graph illustrates throughout a large span of j values (1-5), the bandgap is consistent with a structure in the C-band and L-band and rarely fluctuates over the 0.75 eV bandgap. The bulk Ge structure shows a structure that fluctuates across a small range of j values (1-3), thus not making this structure useful in the C-band and L-band.

FIG. 5 is a graph demonstrating the tensile strain in Ge shrinking its direct bandgap ( $E_g$ ). The theoretical limit of  $E_g$  shrinkage is 0.757 eV corresponding to 1638 nm, which is beyond the L-band edge (1608 nm) of Er amplifiers. The

maximum strained accumulated during the cooling process previously discussed is 0.0032 based on the reported expansion coefficients of Ge and Si. The tensile strain of Ge can function as a universal photodetector for the S+C+L bands of Er doped amplifiers. The same properties can also be seen using a tensile strain SiGe layer.

5        FIG. 6 is a X-ray locking curve measurement of the annealed tensile Ge epilayer. The X-ray locking measurement includes both a tensile Ge epilayer and an unannealed Ge layer, and measures the wavelength of an X-ray arriving at the sampled at a specified angle. This establishes the lattice constant of the samples. FIG. 5 shows that the lattice constant of the Ge epilayer is indeed larger than bulk Ge  
10    by 0.2 %.

Another technique to more effectively utilize the thermal mismatch in the Ge epilayers is to grow Ge on a substrate called "Si on Quartz". Since the lattice constant expansion coefficient of quartz is one order of magnitude smaller than Si, larger strain should be accumulated in the Ge epilayers. The invention is also applicable for  
15    SiGeC as well.

Furthermore, another advantage of this tensile strained Ge is its light hole band. The valence band now consists of light hole bands whose density of state is much smaller than the heavy hole bands. This results in lower saturation current in diode reverse characteristics, which are useful in photodetectors devices, and a faster  
20    drift velocity of holes. This is beneficial in the Ge photodetectors operating in the drift limited regime, not in RC limited regime. In the drift-limited regime, the device response time is limited by slow holes. However, light hole is as fast as electron in the  $\Gamma$  point, so the response time becomes the shortest.

Although the present invention has been shown and described with respect to  
25    several preferred embodiments thereof, various changes, omissions and additions to the form and detail thereof, may be made therein, without departing from the spirit and scope of the invention.

What is claimed is:

CLAIMS

- 1 1. A photodetector device comprising:  
2 a substrate; and  
3 a plurality of Ge epilayers that are grown on said substrate and annealed in a  
4 defined temperature range, said Ge epilayers forming a tensile strained Ge layer that  
5 allows said photodetector device to operate efficiently in the C-band and L-band.
- 1 2. The photodetector device of claim 1, wherein said Ge epilayers are 1-micron  
2 thick.
- 1 3. The photodetector device of claim 1, wherein said Ge epilayers are  
2 approximately 0.5-micron thick.
- 1 4. The photodetector device of claim 1, wherein said tensile strained Ge layer  
2 shows bandgap shrinkage under tensile strain approximately 0.2 %.
- 1 5. The photodetector device of claim 1, wherein said tensile strained Ge layer  
2 shows bandgap shrinkage under tensile strain between approximately 0.2 % and  
3 0.32 %.
- 1 6. The photodetector device of claim 1, wherein said substrate is a silicon substrate.
- 1 7. The photodetector device of claim 6, wherein said epilayers are grown on said  
2 silicon substrate at 700 °C .
- 1 8. The photodetector device of claim 1, wherein said epilayers are grown on said  
2 substrate at a temperature of approximately over 600 °C .
- 1 9. The photodetector device of claim 7, wherein said epilayers are annealed at said  
2 defined temperature between 700 °C and 900 °C .
- 1 10. The photodetector device of claim 8, wherein said epilayers are annealed using  
2 isothermal annealing at said defined temperature of 900 °C .
- 1 11. A method of forming a photodetector device, said method comprising:  
2 forming a plurality of Ge epilayers on a substrate;  
3 annealing said Ge epilayers in a defined temperature range; and



4           developing a tensile strained Ge layer using said annealed Ge epilayers, said  
5   tensile strained Ge layer allowing said photodetector device to operate efficiently in  
6   the C-band and L-band.

1 12. The method of claim 11, wherein said Ge epilayers are 1-micron thick.

1 13. The method of claim 11, wherein said Ge epilayers are approximately 0.5-  
2 micron thick.

1 14. The method of claim 11, wherein said tensile strained Ge layer shows bandgap  
2 shrinkage under tensile strain approximately 0.2 %.

1 15. The method of claim 11, wherein said tensile strained Ge layer shows bandgap  
2 shrinkage under tensile strain between approximately 0.2 % and 0.32 %.

1 16. The method of claim 11, wherein said substrate is a silicon substrate.

1 17. The method of claim 16, wherein said epilayers are grown on said silicon  
2 substrate at 700 °C.

1 18. The method of claim 11, wherein said epilayers are grown on said substrate at a  
2 temperature of approximately over 600 °C .

1 19. The method of claim 17, wherein said epilayers are annealed at said defined  
2 temperature between 700 °C and 900 °C .

1     20. The method of claim 18, wherein said epilayers are annealed using isothermal  
2     annealing at said defined temperature of 900 °C .

1     21. A photodetector device comprising:

2 a substrate; and

3 a plurality of SiGe epilayers that are grown on said substrate at a high  
4 temperature so as to form a SiGe structure, said SiGe epilayers forming a tensile  
5 strained SiGe layer by cooling to room temperature the SiGe structure using bi-  
6 metal effect, said tensile strained SiGe layer allowing said photodetector device to  
7 operate efficiently in the C-band and L-band.

1 22. The photodetector device of claim 21, wherein said SiGe epilayers are 1-micron  
2 thick.

1 23. The photodetector device of claim 21, wherein said SiGe epilayers are  
2 approximately 0.5-micron thick.

1 24. The photodetector device of claim 21, wherein said tensile strained SiGe layer  
2 shows bandgap shrinkage under tensile strain between approximately 0.2 % and  
3 0.32 %.

1 25. The photodetector device of claim 21, wherein said substrate is a silicon  
2 substrate.

1 26. The photodetector device of claim 25, wherein said epilayers are grown on said  
2 silicon substrate at 700 °C .

1 27. The photodetector device of claim 26, wherein said epilayers are annealed  
2 using isothermal annealing at said defined temperature of 1330 °C .

1 28. A method of forming a photodetector device, said method comprising:  
2 growing a plurality of SiGe epilayers on a silicon substrate at a high  
3 temperature so as to form a SiGe structure; and  
4 forming a tensile strained SiGe layer by cooling to room temperature the  
5 SiGe structure using bi-metal effect, said tensile strained SiGe layer allowing said  
6 photodetector device to operate efficiently in the C-band and L-band.

1 29. The method of claim 28, wherein said Ge epilayers are 1-micron thick.

1 30. The method of claim 28, wherein said Ge epilayers are approximately 0.5-  
2 micron thick.

1 31. The method of claim 28, wherein said tensile strained Ge layer shows bandgap  
2 shrinkage under tensile strain approximately 0.2 %.

1 32. The method of claim 28 wherein said tensile strained Ge layer shows bandgap  
2 shrinkage under tensile strain between approximately 0.2 % and 0.32 %.

1 33. The method of claim 28, wherein said substrate is a silicon substrate.

- 1 34. The method of claim 33, wherein said epilayers are grown on said silicon  
2 substrate at 700 °C .
- 1 35. The method of claim 34, wherein said epilayers are annealed using isothermal  
2 annealing at said defined temperature of 1330 °C .

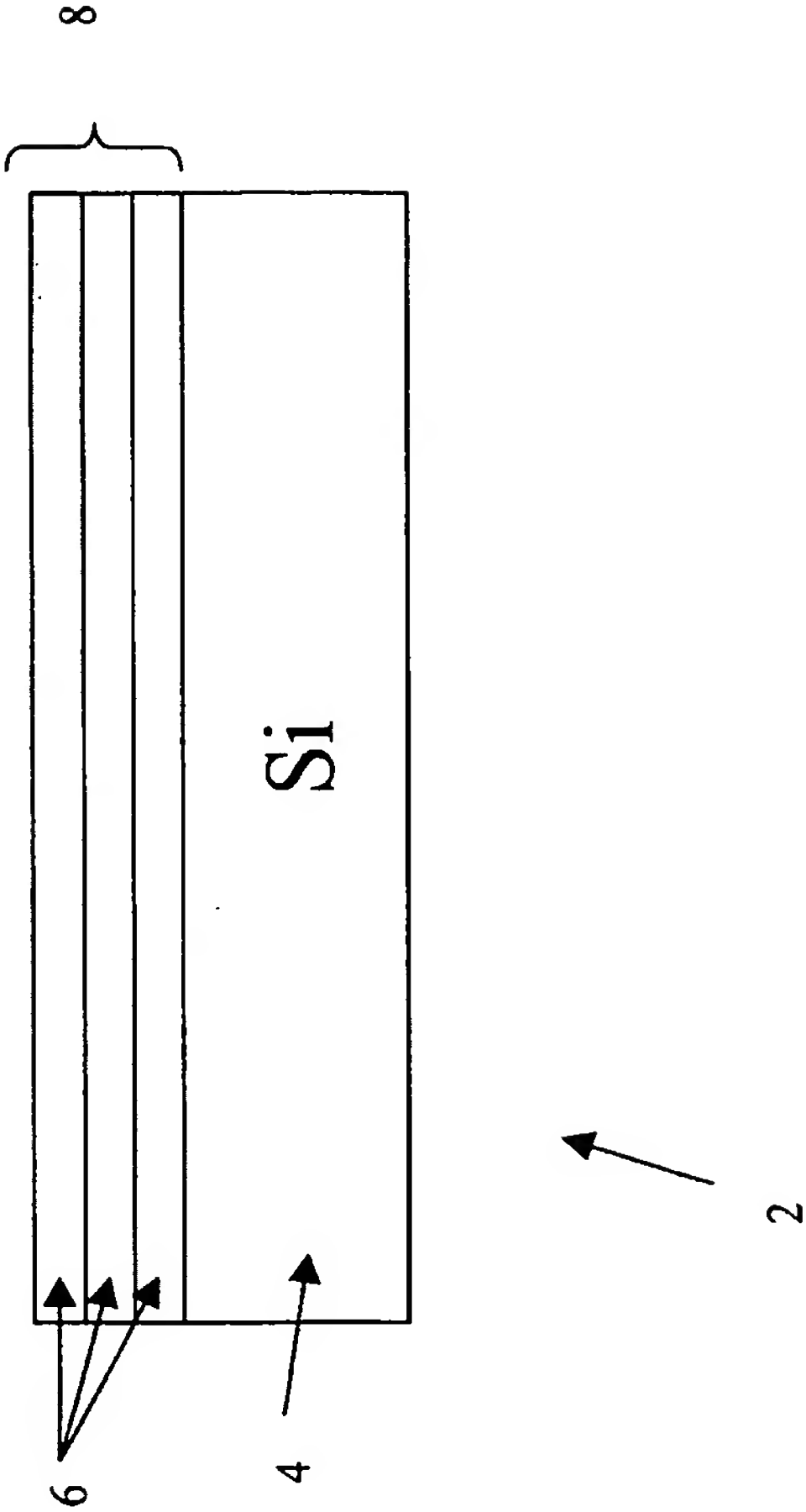


FIG. 1

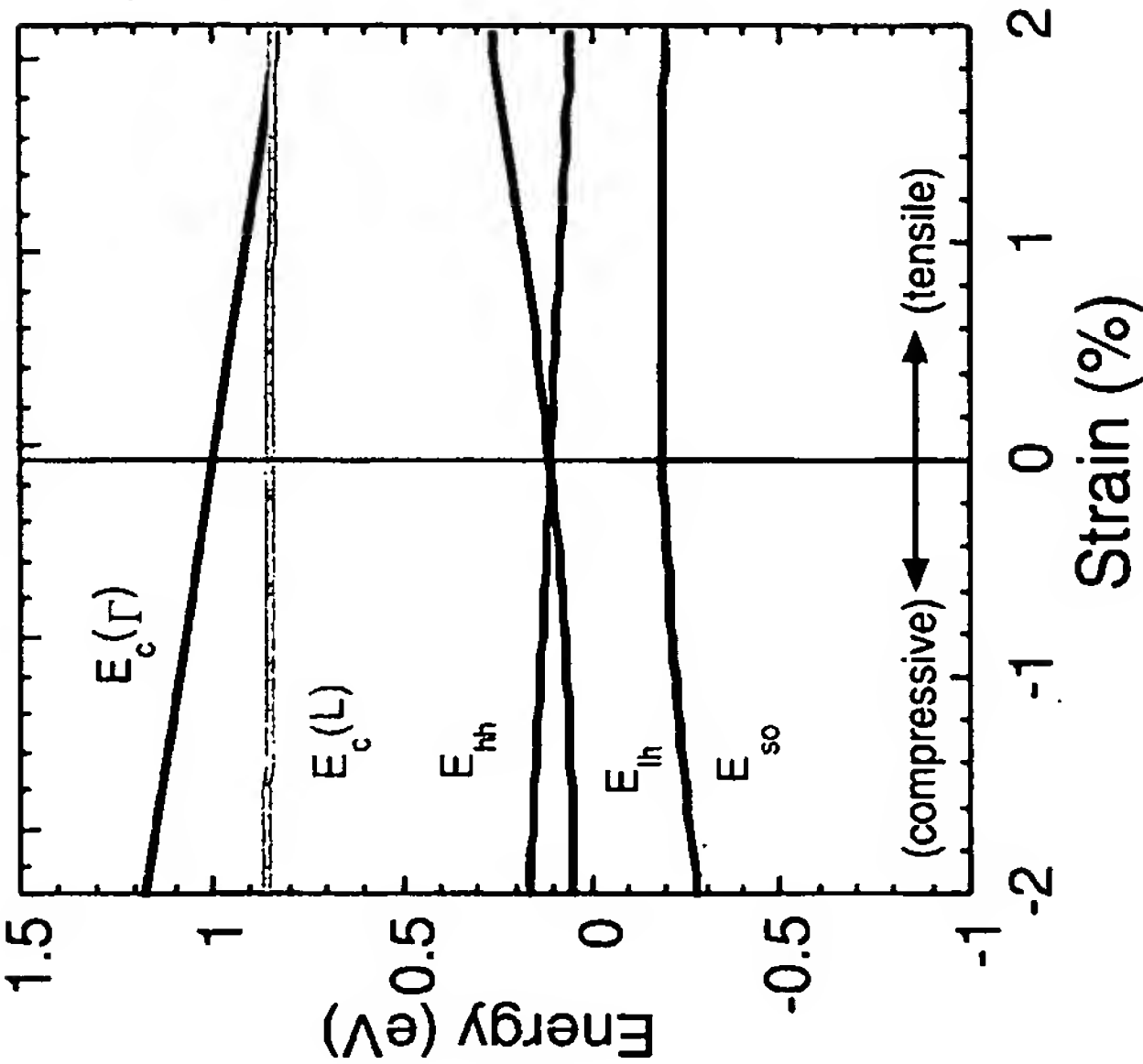


FIG. 2



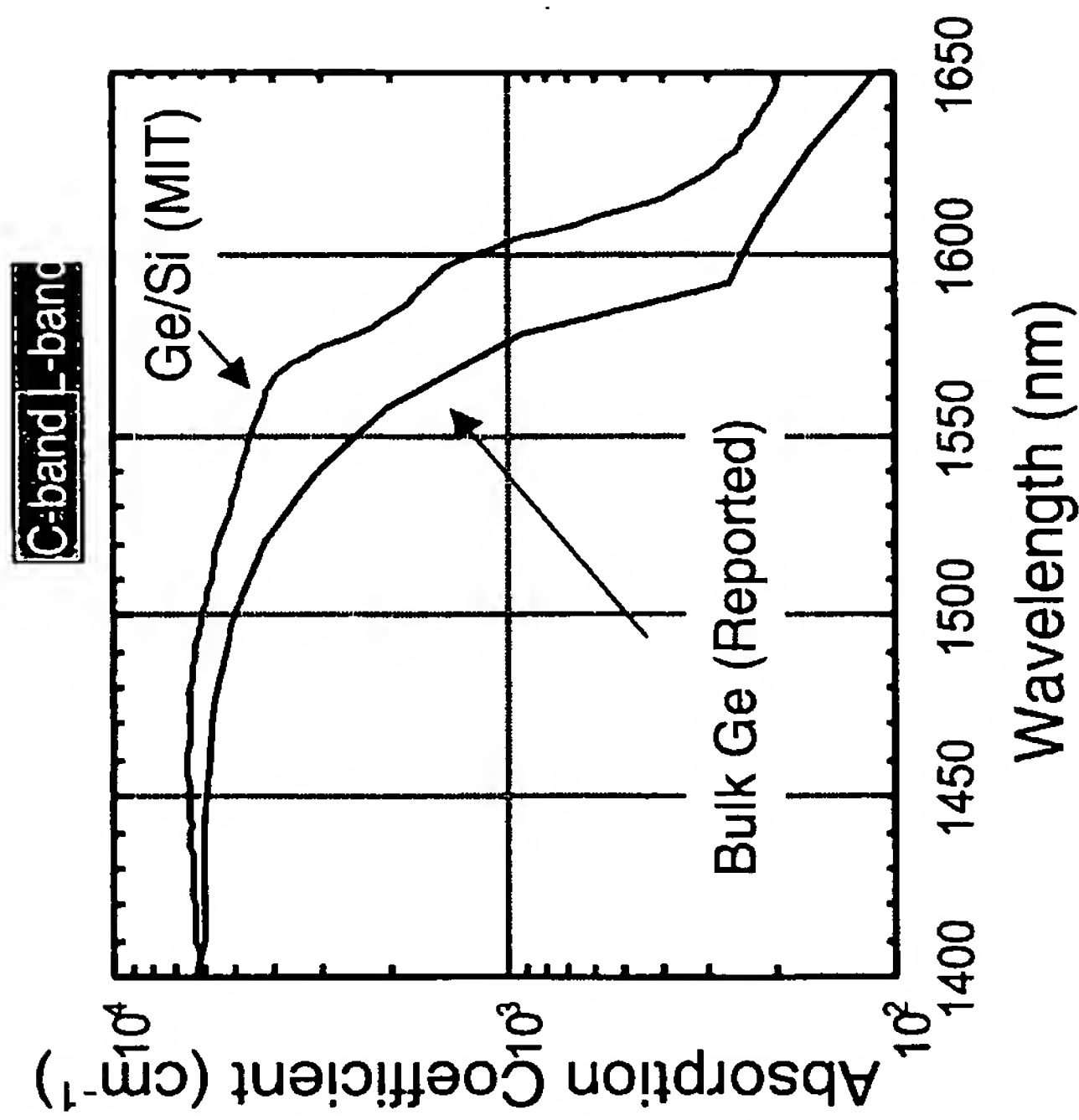


FIG. 3

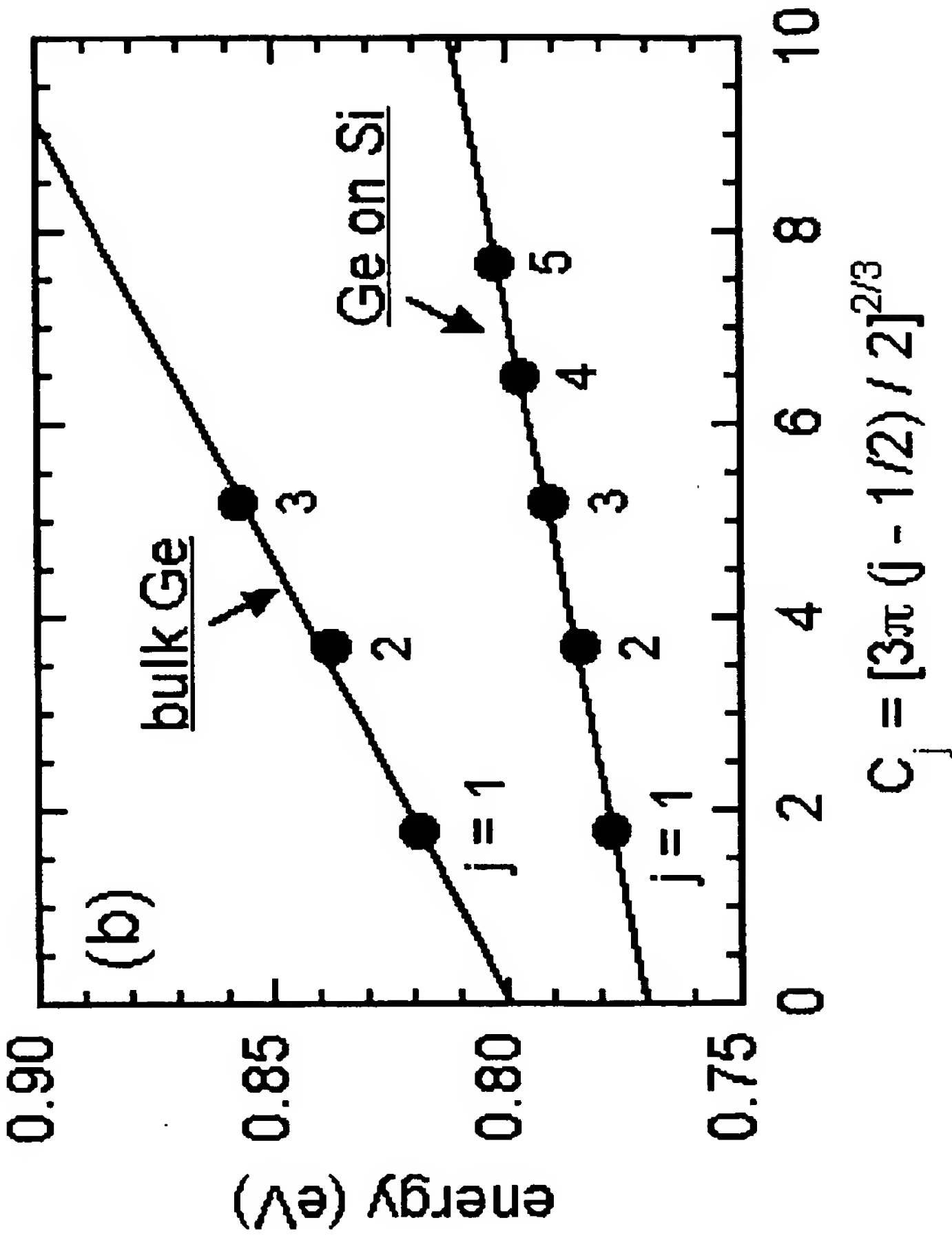


FIG. 4

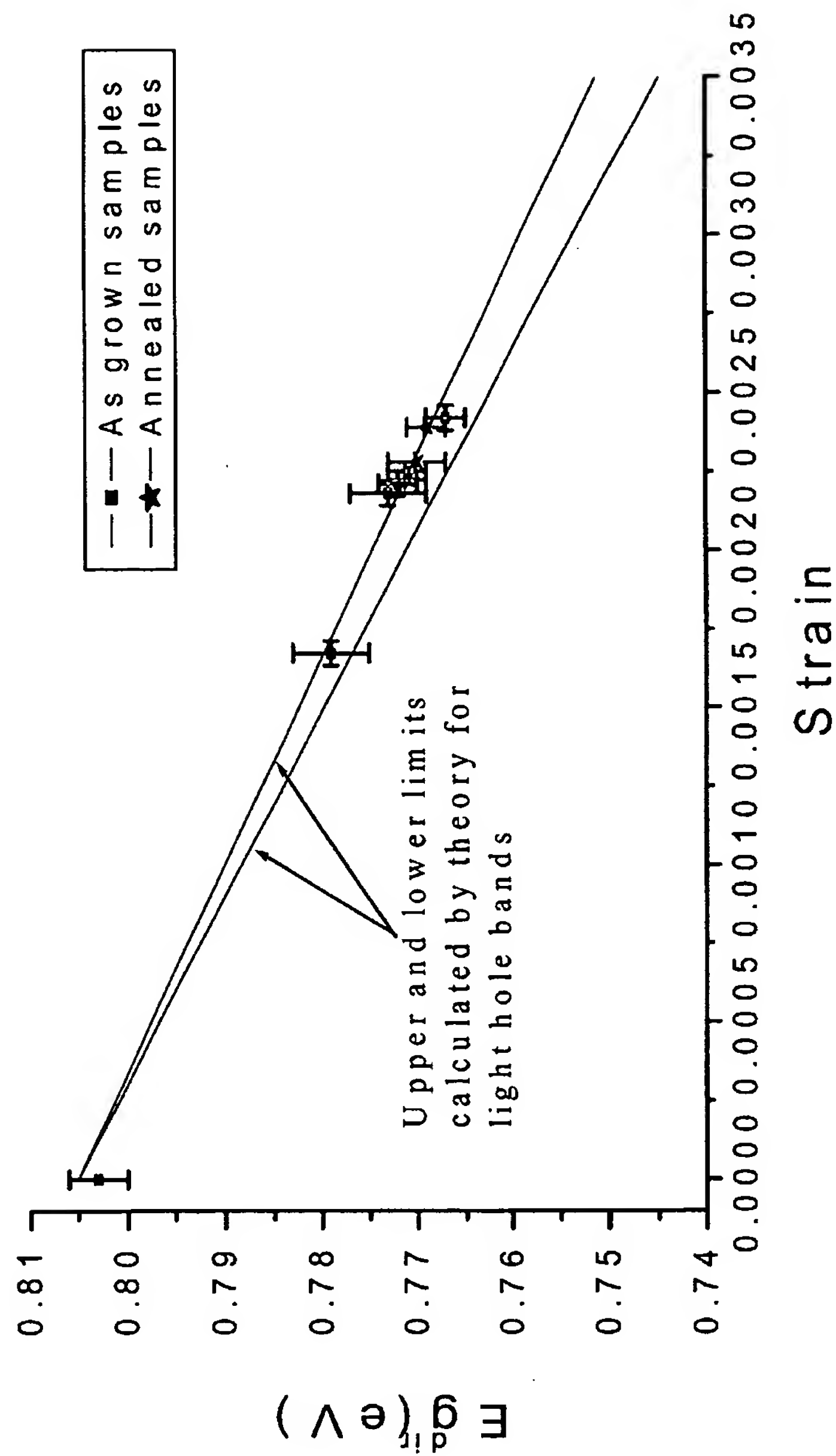


FIG. 5

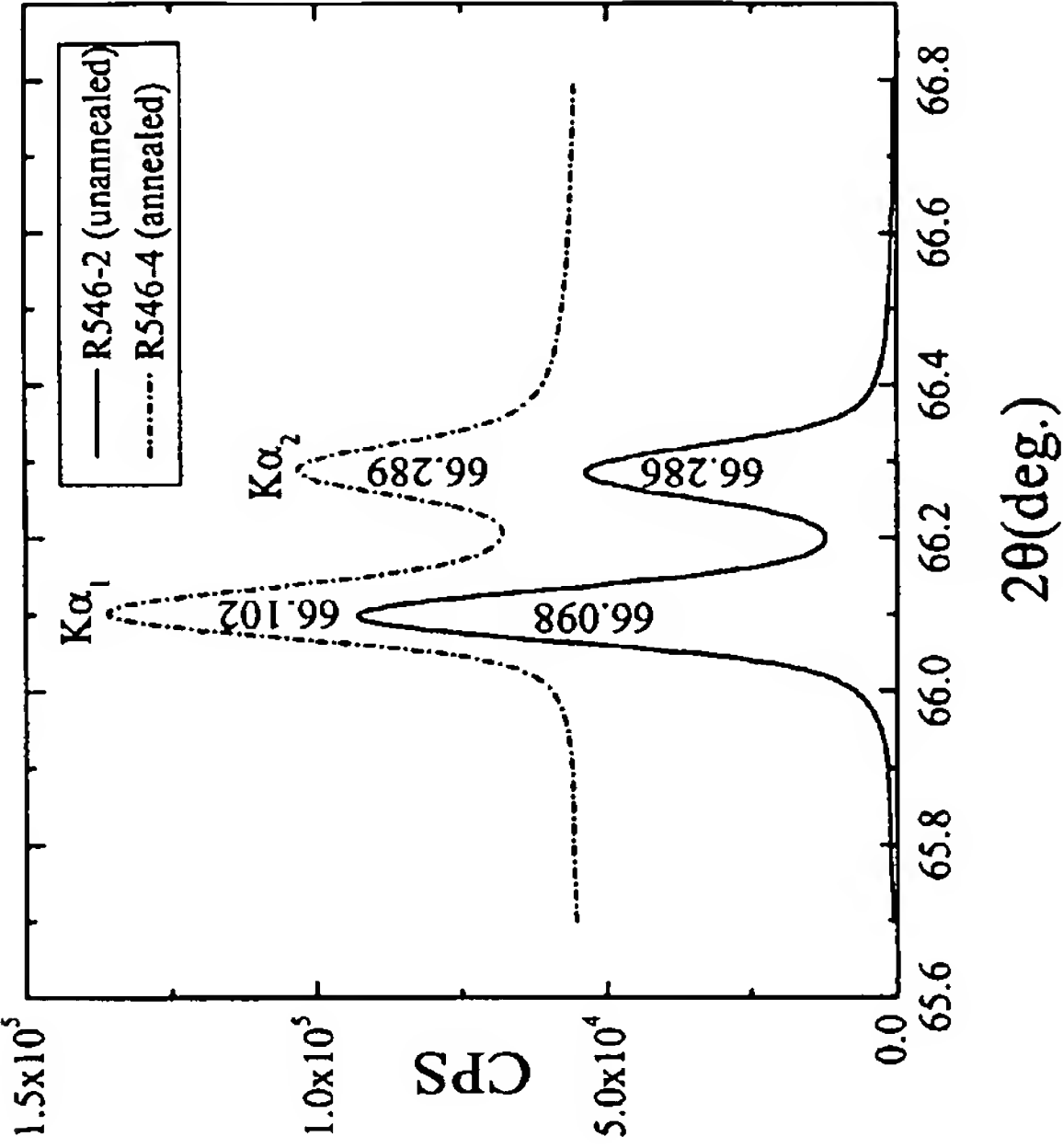


FIG. 6

## INTERNATIONAL SEARCH REPORT

International Application No

PCT/US 02/38283

A. CLASSIFICATION OF SUBJECT MATTER  
IPC 7 H01L31/028 H01L31/18

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)  
IPC 7 H01L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, INSPEC

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	EP 0 224 189 A (AMERICAN TELEPHONE & TELEGRAPH) 3 June 1987 (1987-06-03)  column 5, line 9-27; claim 1 column 7, line 25-33 ---	1-8, 11-18, 21-26, 28-34
X	EP 0 940 854 A (IBM) 8 September 1999 (1999-09-08)  paragraph '0038!; claims --- -/--	1-8, 11-18, 21-26, 28-34



Further documents are listed in the continuation of box C.



Patent family members are listed in annex.

## \* Special categories of cited documents :

- \*A\* document defining the general state of the art which is not considered to be of particular relevance
- \*E\* earlier document but published on or after the international filing date
- \*L\* document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)
- \*O\* document referring to an oral disclosure, use, exhibition or other means
- \*P\* document published prior to the international filing date but later than the priority date claimed

- \*T\* later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
- \*X\* document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
- \*Y\* document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.
- \*&\* document member of the same patent family

Date of the actual completion of the international search

10 September 2003

Date of mailing of the international search report

17/09/2003

Name and mailing address of the ISA

European Patent Office, P.B. 5818 Patentlaan 2  
NL - 2280 HV Rijswijk  
Tel. (+31-70) 340-2040, Tx. 31 651 epo nl,  
Fax: (+31-70) 340-3016

Authorized officer

Wolff, G



## INTERNATIONAL SEARCH REPORT

International Application No

PCT/US 02/38283

## C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 6 331 445 B1 (LAFONTAINE HUGHES ET AL) 18 December 2001 (2001-12-18)  column 1, line 57-64 column 6, line 57 -column 7, line 25 ---	1-8, 11-18, 21-26, 28-34
X	US 6 222 200 B1 (SVILANS MIKELIS NILS) 24 April 2001 (2001-04-24)  column 2, line 65 -column 3, line 11; claim 11 ---	1-8, 11-18, 21-26, 28-34
A	JAIN S C ET AL: "STRUCTURE, PROPERTIES AND APPLICATIONS OF GEXSI1-X STRAINED LAYERS AND SUPERLATTICES" SEMICONDUCTOR SCIENCE AND TECHNOLOGY, INSTITUTE OF PHYSICS. LONDON, GB, vol. 6, no. 7, 1 July 1991 (1991-07-01), pages 547-576, XP000292317 ISSN: 0268-1242 chapter 5.2.1. Optical detectors ---	1-35
T	US 2003/146428 A1 (EVANS DAVID RUSSELL ET AL) 7 August 2003 (2003-08-07) paragraphs '0028!, '0035! -----	1-35

# INTERNATIONAL SEARCH REPORT

Information on patent family members

International Application No

PCT/US 02/38283

Patent document cited in search report		Publication date	Patent family member(s)	Publication date
EP 0224189	A	03-06-1987	CA 1282671 C DE 3684247 D1 EP 0224189 A2 JP 62128183 A US 4772924 A	09-04-1991 16-04-1992 03-06-1987 10-06-1987 20-09-1988
EP 0940854	A	08-09-1999	EP 0940854 A2 JP 3007892 B2 JP 11284220 A TW 415103 B US 2002171077 A1	08-09-1999 07-02-2000 15-10-1999 11-12-2000 21-11-2002
US 6331445	B1	18-12-2001	NONE	
US 6222200	B1	24-04-2001	NONE	
US 2003146428	A1	07-08-2003	NONE	